

Electronic Acknowledgement Receipt

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Title of Invention:	METHODS AND STRUCTURES FOR PLANAR AND MULTIPLE-GATE TRANSISTORS FORMED ON SOI
First Named Inventor/Applicant Name:	Fu-Liang Yang
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)

1	Issue Fee Payment (PTO-85B)	TSM03_0929_IssueFee.pdf	94727	no	1
Warnings:					
Information:					
2	Fee Worksheet (PTO-06)	fee-info.pdf	8325	no	2
Warnings:					
Information:					
Total Files Size (in bytes):					103052
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